

## **Amendments to the Specification**

***Please replace the paragraph inserted after the title and before the heading***

***"BACKGROUND OF THE INVENTION" with the following amended paragraph:***

### **CROSS REFERENCE TO RELATED APPLICATIONS**

This is a divisional application of application Serial No. 09/584,549, filed June 1, 2000, now abandoned, which is hereby incorporated by reference in its entirety for all purposes.

***Please replace the paragraph beginning on page 1, line 9 with the following amended paragraph:***

~~Fig. 5 is a diagram~~ Figs. 5(A) – 5(C) are diagrams illustrating conventional transposition of matrix data. Fig. 5A is a block diagram depicting the structure of a transposition circuit. Fig. 5B is an image diagram depicting memory content. Fig. 5C is a timing chart depicting the operation of a transposition circuit.

***Please replace the paragraph beginning on page 10, line 14 with the following amended paragraph:***

~~Fig. 5 (which includes Figs. 5A, 5B, and 5C) is a diagram~~ 5C are diagrams illustrating conventional transposition of matrix data.